IN THE SPECIFICATION:

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Please amend the specification as follows:

Paragraph beginning on page 2, at prenumbered line 14, has been amended as follows:

Figure 1 is an operating flow chart of the present invention.

Figure 2 is a drawing Figures 2A and 2B are drawings of the logic circuit and oscillogram of the present invention.

Figure 3 is a drawing Figures 3A and 3B are drawings of the logic circuit and the oscillogram of the first exemplary embodiment of the present invention.

Figure 4 is a drawing Figures 4A and 4B are drawings of the logic circuit and the oscillogram of the second exemplary embodiment of the present invention.

Figure 5 is a drawing Figures 5A and 5B are drawings of the logic circuit and the oscillogram of the third exemplary embodiment of the present invention.

Figure 6 is a drawing Figures 6A and 6B are drawings of the logic circuit and the oscillogram of the fourth exemplary embodiment of the present invention.

Figure 7 is a drawing Figures 7A and 7B are drawings of the logic circuit and the oscillogram of the fifth exemplary embodiment of the present invention.

Figure 8 is a drawing Figures 8A and 8B are drawings of the logic circuit and the oscillogram of the sixth exemplary embodiment of the present invention.